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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Nishant Sinha

Serial No.: 10/668,914

Filed: September 23, 2003

For: PROCESS AND INTEGRATION
SCHEME FOR FABRICATING
CONDUCTIVE COMPONENTS,
THROUGH-VIAS AND
SEMICONDUCTOR COMPONENTS
INCLUDING CONDUCTIVE THROUGH-
WAFER VIAS

Confirmation No.: 2525

Examiner: W. Lindsay Jr.

Group Art Unit: 2812

Attorney Docket No.: 2269-5859US
(02-0390.00/US)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 16, 2005
Date

Signature

Shirley Dougherty
Name (Type/Print)

RESPONSE TO RESTRICTION AND ELECTION OF SPECIES REQUIREMENT

MAIL STOP AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This communication is submitted in response to the Restriction and Election of Species Requirement of February 4, 2005, having an initial period of response expiring on March 4, 2005.

Claims 1-72 are currently pending in the application and subject to a Restriction Requirement. Responsive to the Restriction Requirement, applicant hereby elects, without

traverse, to prosecute claims 25-40 and 64-69 of Group I drawn to semiconductor components, classified in class 257, subclass 1+.

As the applicant understands the Restriction and Election of Species Requirement, the Examiner also noted that the “application contains claims directed to the following patentably distinct species of the claimed invention: an intermediate semiconductor component (claims 25-32 and 64-66) and a semiconductor component (claims 33-40 and 67-69).” (Office Action of February 4, 2005, page 3). Although not acquiescing to the Office’s characterization of the claims in the species requirement, applicant hereby elects, without traverse, the intermediate semiconductor component (claims 25-32 and 64-66).

Accordingly, applicant requests an action on the merits of claims 25-40 and 64-69 of Group I.

Please Note Information Disclosure Statements

Applicant wishes to draw the Examiner's attention to the Information Disclosure Statements filed with the Office on September 23, 2003, November 7, 2003, and October 4, 2004, and respectfully requests that the documents or other information referenced therein be made of record in the present application and that an initialed copy of each PTO-1449 or PTO/SB/08 form be returned to the undersigned attorney evidencing same.

Should any of the documents, or portions thereof, be unavailable to the Examiner for any reason, please contact the undersigned attorney, who will supply the same to the Examiner immediately by facsimile or other suitable method of delivery.

Preliminary Amendments

Applicant’s undersigned attorney notes the filing of a Preliminary Amendment on February 9, 2004, and a Supplemental Preliminary Amendment on September 13, 2004, which were not acknowledged in the outstanding Communication from the Office. Should the Preliminary Amendments have failed for some reason to have been entered in the Office file, Applicant’s undersigned attorney will be happy to have a true copy of each hand-delivered to the Examiner.

CONCLUSION

Should questions remain after consideration of the foregoing, the Office is kindly requested to contact the applicant's attorney at the address or telephone number herein.

Respectfully submitted,



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Date: February 16, 2005
AFN/ps:sd
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